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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

David E. McKinley

ee:

Zilog, Inc.

"DYNAMIC POWER MANAGEMENT OF SOLID STATE

MEMORIES"

Serial No.:

07/910.213

Filed: July 9, 1992

Examiner:

G. Davis

2308 Group Art Unit:

Attorney Docket No.: M-2689 US

San Jose, California

November 3, 1994

BOX AF COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D. C. 20231

AMENDMENT AFTER FINAL OFFICE ACTION

Sir:

In response to the Final Office Action mailed August 4, 1994, please amend the above-identified patent application as set forth below.

IN THE CLAIMS

(Twice Amended) A dynamic power management device for supplying power to a solid state memory integrated circuit in an electronic system, said device comprising:

power control means for supplying a variable voltage to said memory integrated circuit independently from a [the] voltage being supplied to the portions [rest] of said electronic system other than said memory integrated circuit; and

logic control means for generating address and control signals for said memory integrated circuit and for controlling said power control means to supply power to said memory integrated circuit at a level to maintain memory information in said memory integrated circuit during periods of no data access activity and supplying

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